ABSTRACT

A power splitter has a small size with good electrical performance. The power splitter includes a substrate with several dielectric layers. A capacitor is formed between two of the dielectric layers. A pair of inductors are formed by circuit lines located on the dielectric layers. Several terminals are located on outer surfaces of the substrate. Conductive vias extend between the layers in order to provide an electrical connection between the capacitor, the inductors and the terminals. The substrate is formed from layers of low temperature co-fired ceramic. The circuit lines have a curving or sinuous shape on the dielectric layers. The inductors are electromagnetically coupled. The power splitter has a compact overall size.

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